


11.8 Supply of modules

| Module slot 1 \ Module slot 2 | | Module slot 2 | | | | | | | | | | | | | | | |
|-------------------------------|---|---------------|----|----|----------------|----------|----|----|----------------|----|----|----|----|----|----|-----------------|--|
| | | 00 | 10 | 20 | 30 32 34 | 50 51 | 11 | 21 | 11 31 33 | 22 | 40 | 41 | 43 | 66 | 67 | 56 ... 85 | |
| 00 | No module | | | | | | | | | | | | | | | | |
| 10 | Data Pulse, inputs | | | | | | | | | | | | | | | | |
| 20 | Wired M-Bus, inputs | | | | | | | | | | | | | | | | |
| 30 32 34 | Wireless M-Bus, inputs linkIQ/wM-Bus | | | | | | | | | | | | | | | | |
| 50 51 | Low Power Radio, inputs | | | | | | | | | | | | | | | | |
| 11 | Data Pulse, outputs | | | | | | | | | | | | | | | | |
| 21 | Wired M-Bus, outputs | | | | | | | | | | | | | | | | |
| 11 31 33 | Outputs as pulstransmitter (V1+V2) | | | | | | | | | | | | | | | | |
| 22 | Wired M-Bus, Thermal Disconnect | | | | | | | | | | | | | | | | |
| 40 | Analog outputs | | | | | | | | | | | | | | | | |
| 41 | Analog inputs | | | | | | | | | | | | | | | | |
| 43 | PQT Controller | | | | | | | | | | | | | | | | |
| 66 | BACnet MS/TP, inputs | | | | | | | | | | | | | | | | |
| 67 | Modbus RTU, inputs | | | | | | | | | | | | | | | | |
| 56 ... 85 | 56,60,82,83,84 and 85 | | | | | | | | | | | | | | | | |

 Battery supply

 Mains supply

 High-power SMPS

Overview of battery lifetimes, of different configurations is shown in paragraph 10.3.